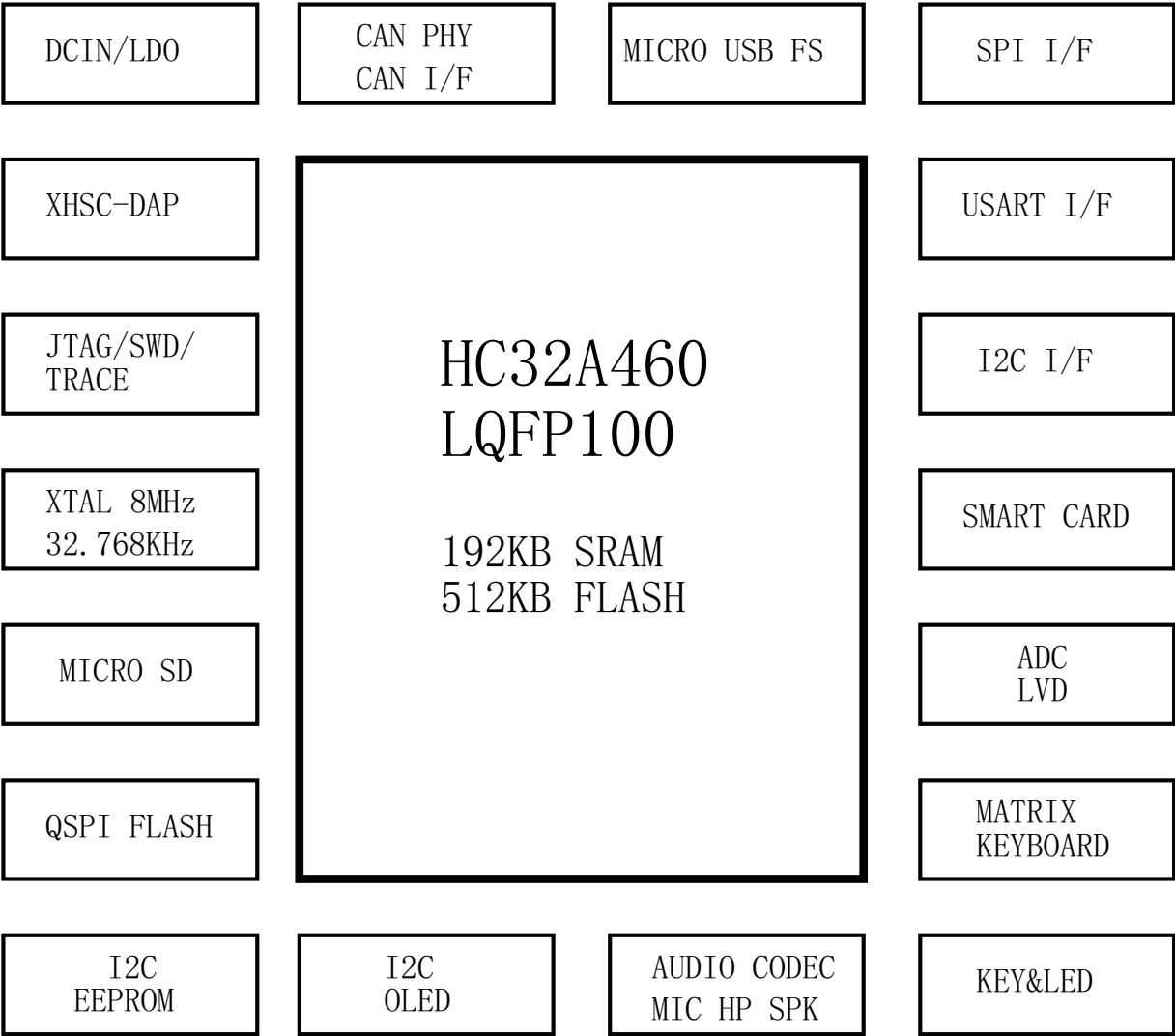


Table of Content

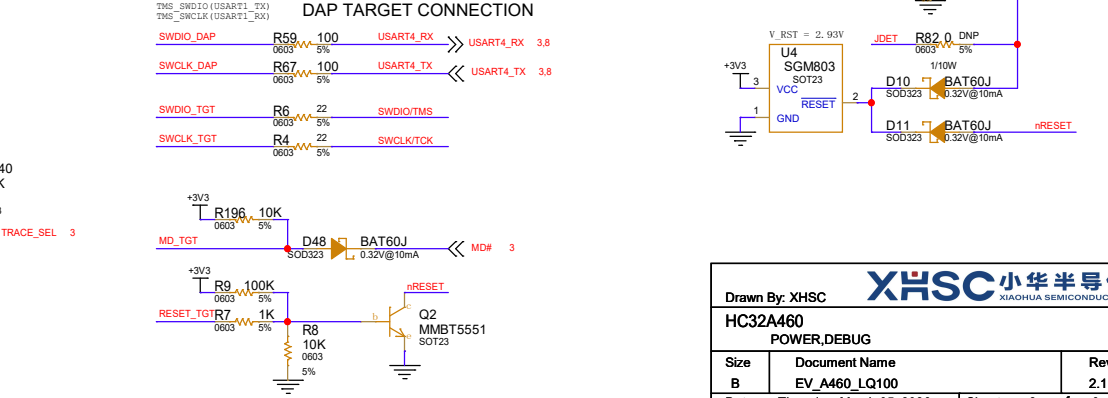
Page 01	TITLE PAGE
Page 02	POWER,DEBUG
Page 03	HC32A460
Page 04	AUDIO
Page 05	QSPI FLASH,MICRO SD,EEPROM
Page 06	USB FS,CAN
Page 07	ADC,KEYS,LEDS
Page 08	USART,SPI,I2C,SMART CARD

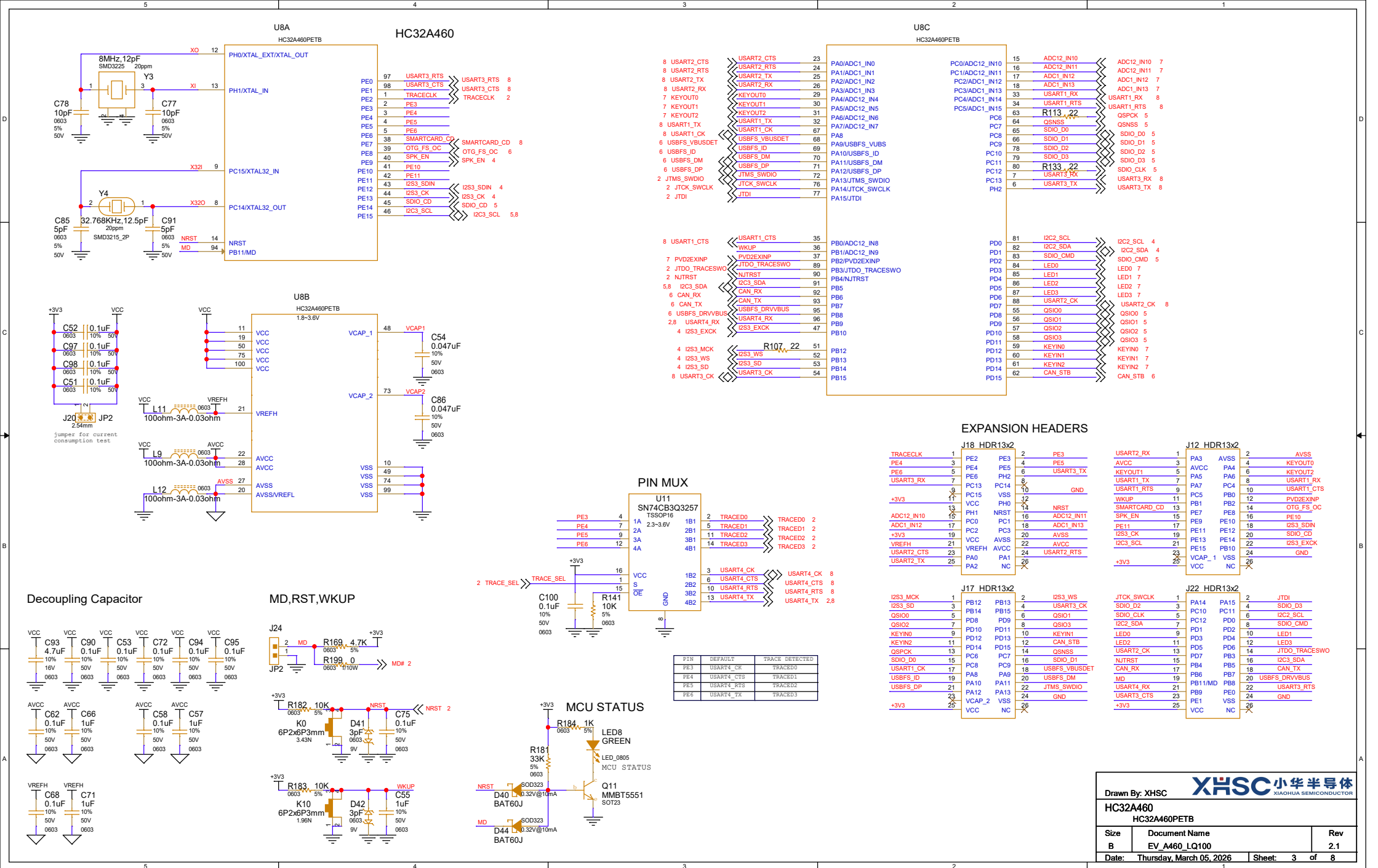
Revision History

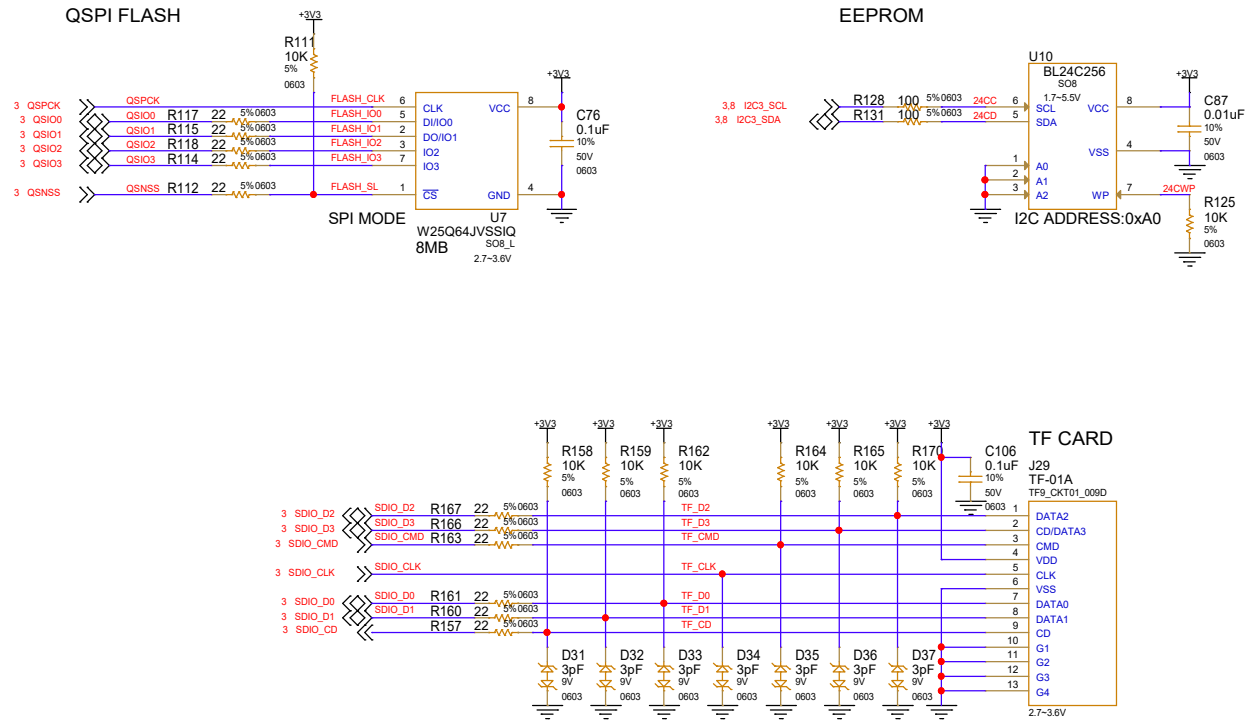
Rev	Date	Description
1.0	08/21/2018	- New design.
1.1	12/12/2018	- Changed UART connector to match SmartCard. - Added R143~R147 to I2S signals. - Added R143 as pull-up resistor to SPI NSS. - Changed matrix keyboard output pin,usart2_ck pin,and LED1 control pin. - Changed CAN and I2C IO pin.
1.2	01/11/2019	- Update HC32A460 pin definition. - C28 is now DNP.
1.3	09/23/2019	- Replaced C72 by R148. - Added C94,C95,C96 to VCCA/VREF. - Removed R25,R26.
2.0	02/23/2022	- DAP module added. - Pin assignment modified. - Analog decoupling capacitor modified.
2.1	09/27/2024	- Update HC32A460 pin definition.



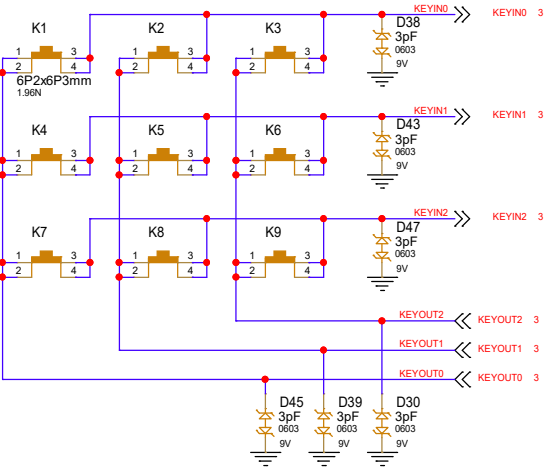
 小华半导体 XIAOHUA SEMICONDUCTOR	
Drawn By: XHSC	
HC32A460 POWER,DEBUG	
Size B	Document Name EV_A460_LQ100
Date: Thursday, March 05, 2026	Rev 2.1
Sheet: 2 of 8	



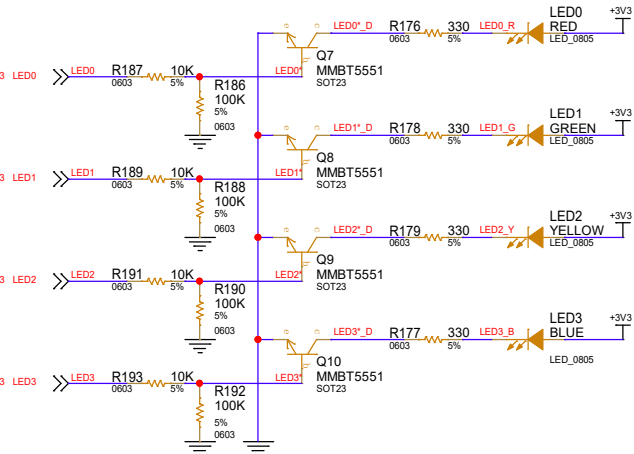




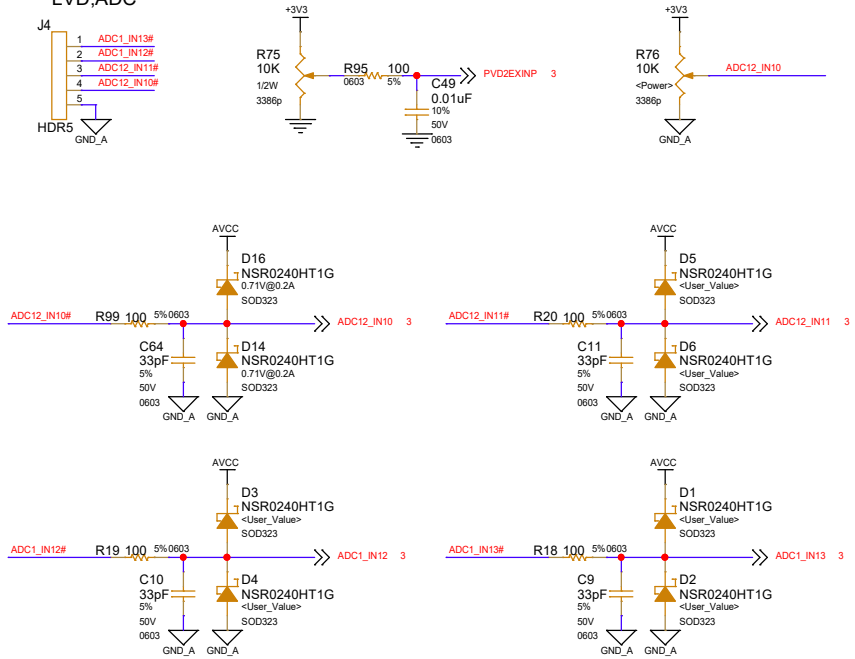
MATRIX KEYBOARD



LEDS

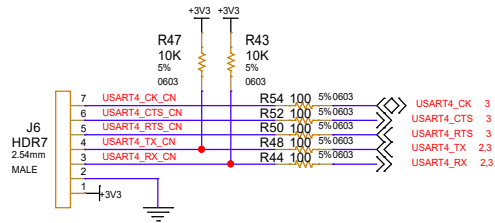
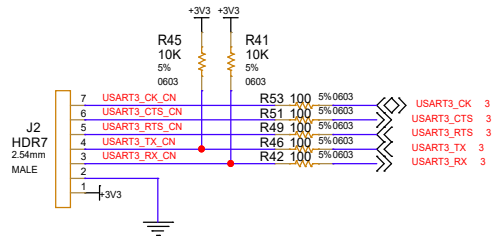
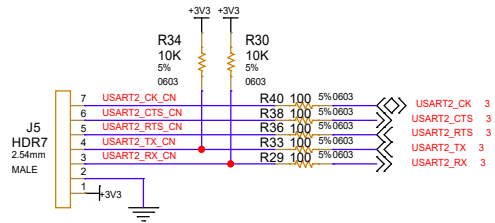
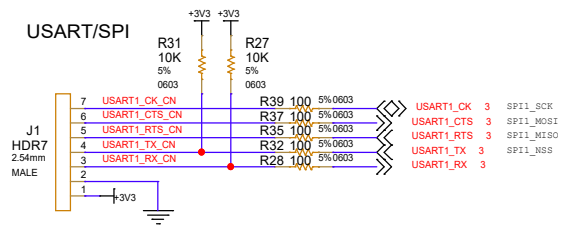


LVD,ADC

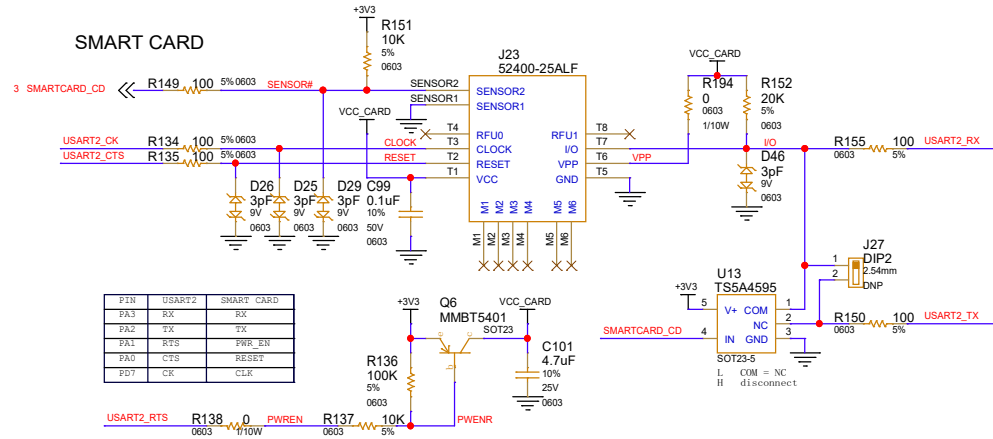


Drawn By: XHSC		
HC32A460		
ADC,KEYS,LEDS		
Size	Document Name	Rev
B	EV_A460_LQ100	2.1
Date:	Thursday, March 05, 2026	Sheet: 7 of 8

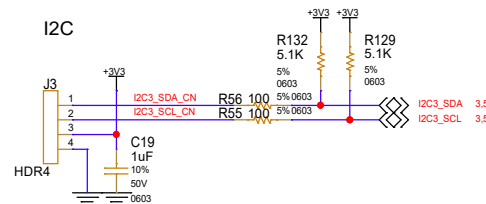
USART/SPI



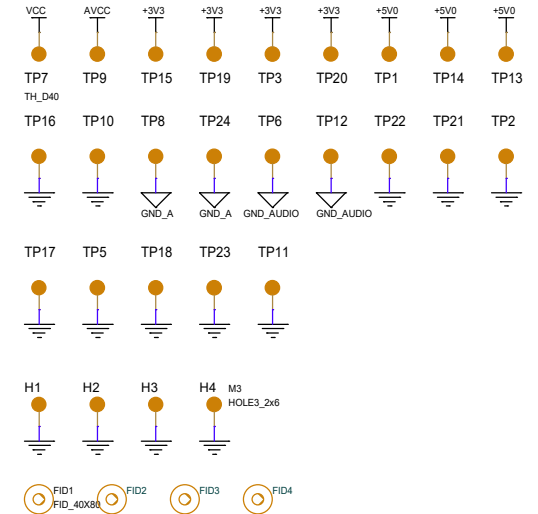
SMART CARD



I2C



TP/FID/FAB HOLES



Drawn By: XHSC

XHSC 小华半导体
XIAOHUA SEMICONDUCTOR

HC32A460

USART,SPI,I2C,SMART CARD

Size
BDocument Name
EV_A460_LQ100Rev
2.1

Date: Thursday, March 05, 2026

Sheet: 8 of 8