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Revision History

Rev	Date	Description
1.0	10/11/2024	- Initial release.

DCIN
LDO

XHSC-DAP

JTAG/SWD
TRACE

XTAL 12MHz
32. 768KHz

POTENTIOMETER

KEYS, LEDS

HC32A472
LQFP100
120MHz
68KB SRAM
512KB FLASH

USBFS
CAN LIN

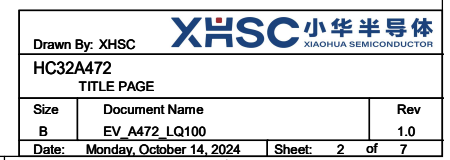
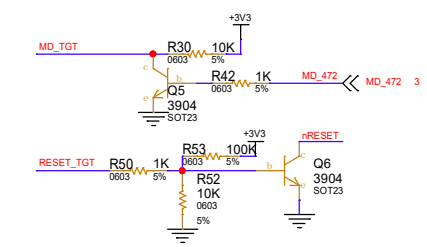
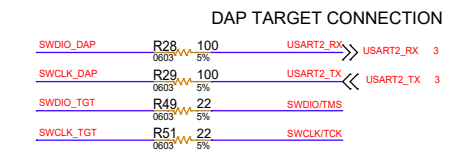
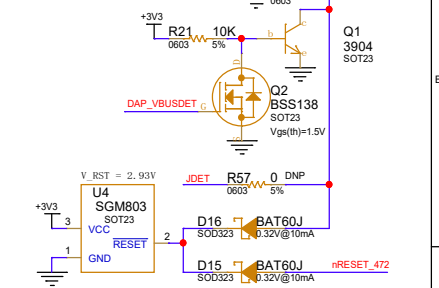
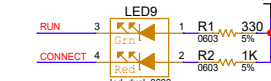
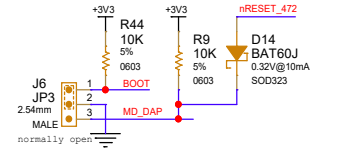
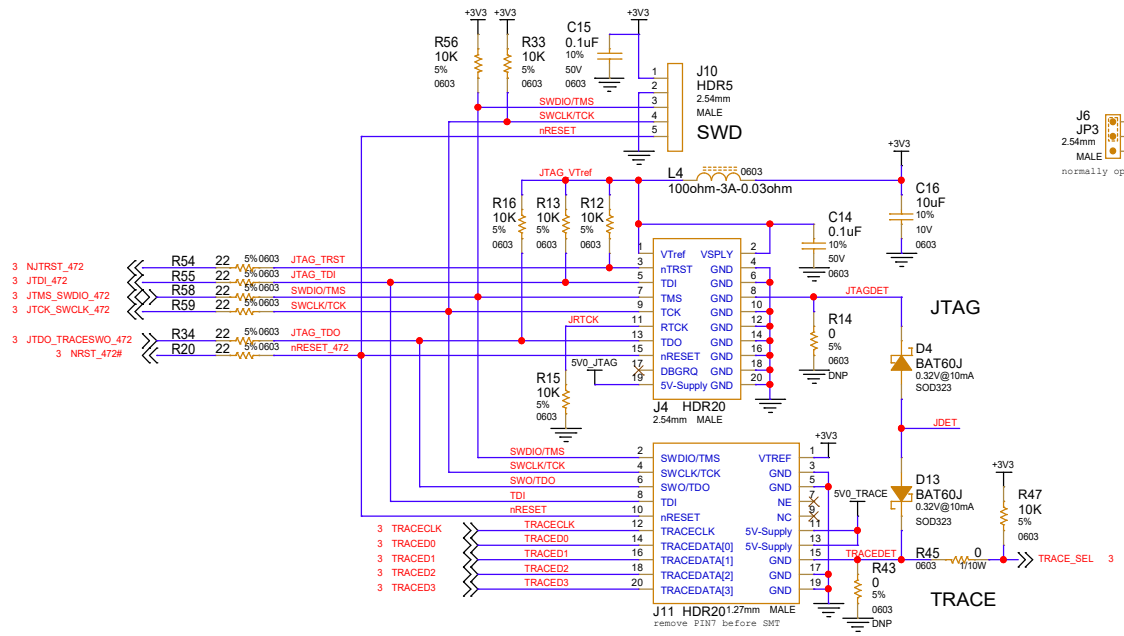
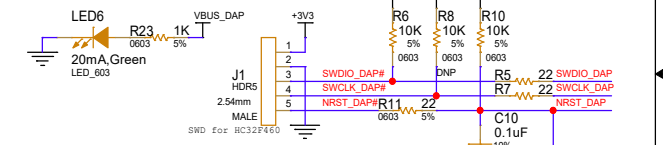
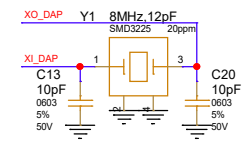
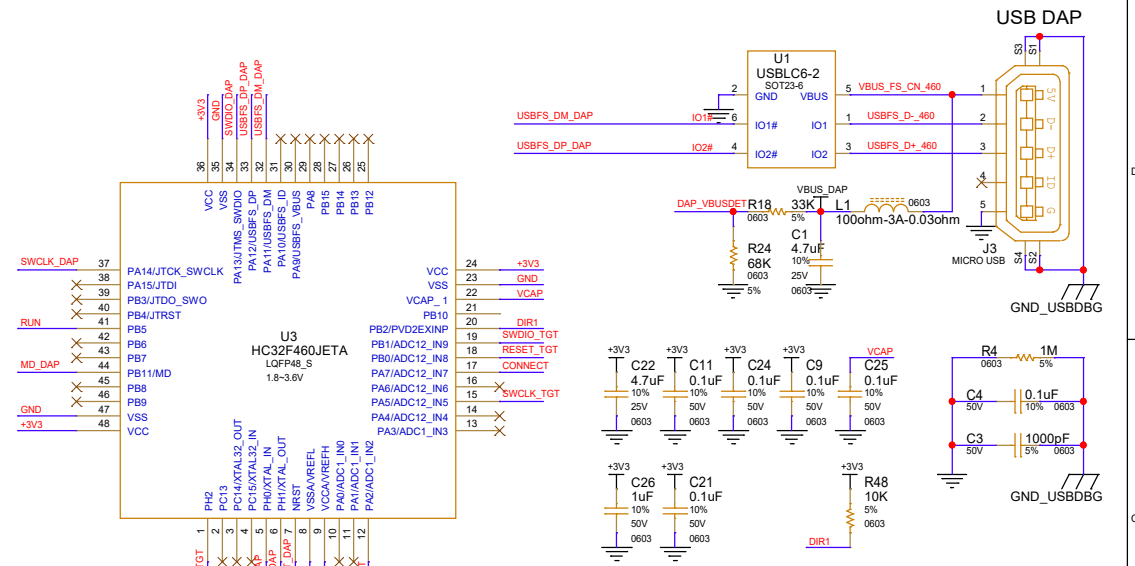
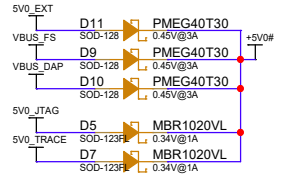
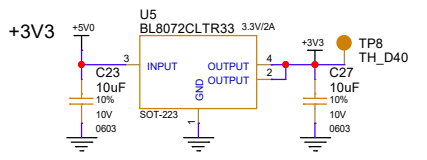
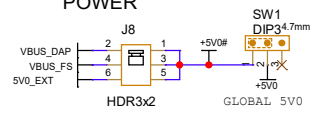
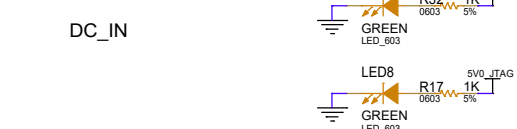
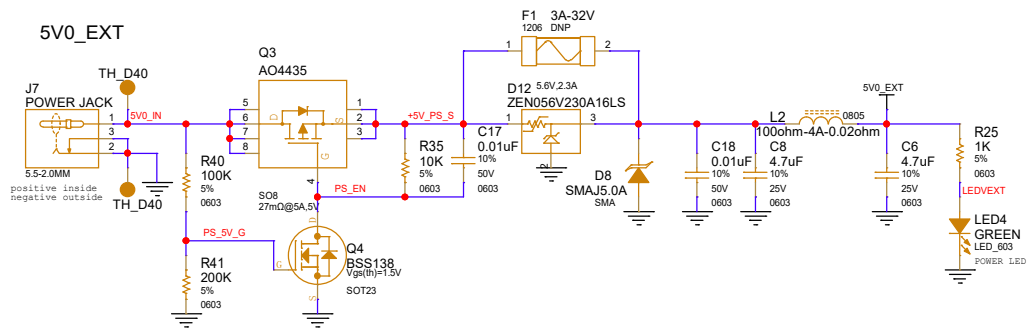
SRAM
LCD I/F

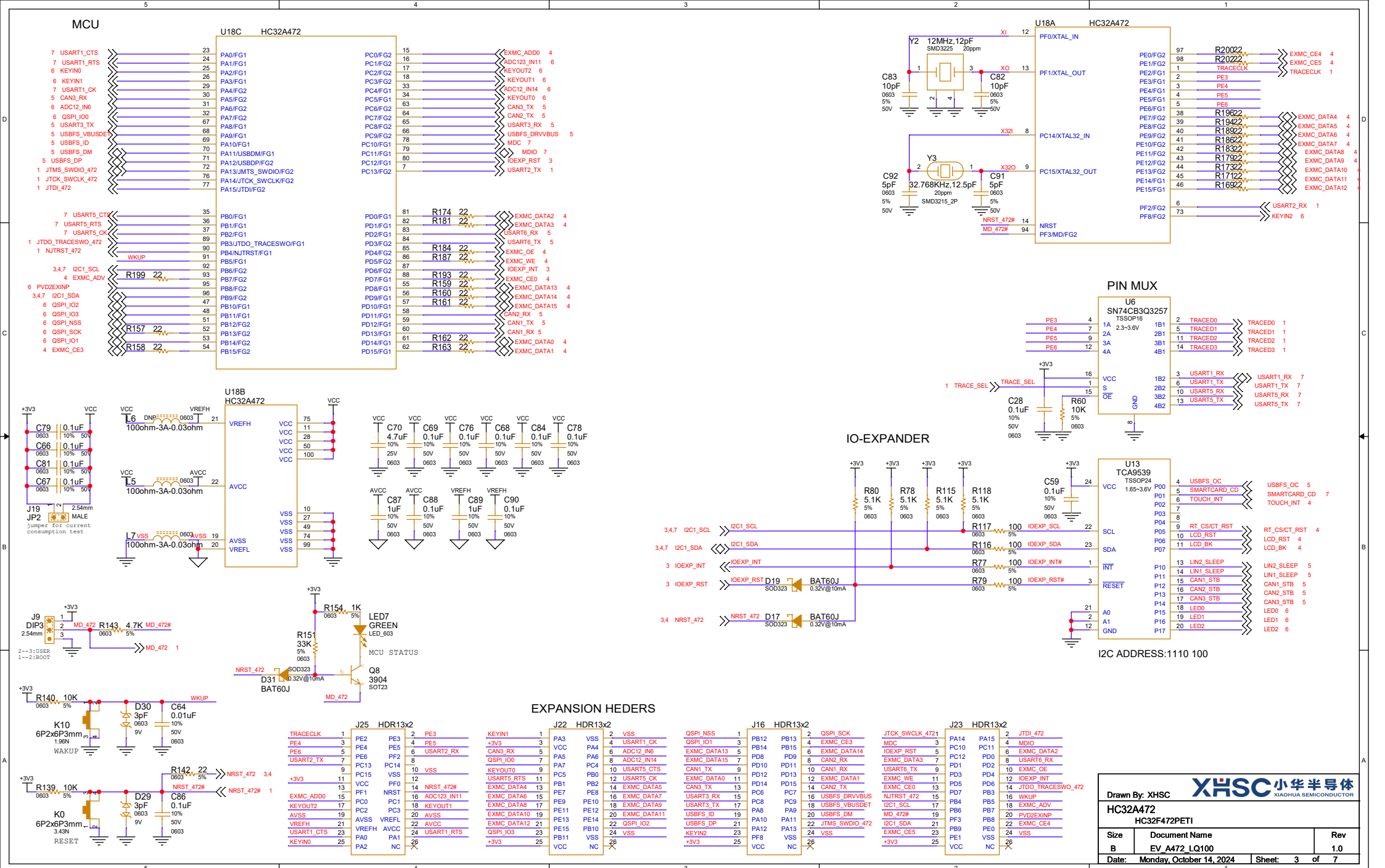
USART SPI
I/F

I2C MDIO
I/F

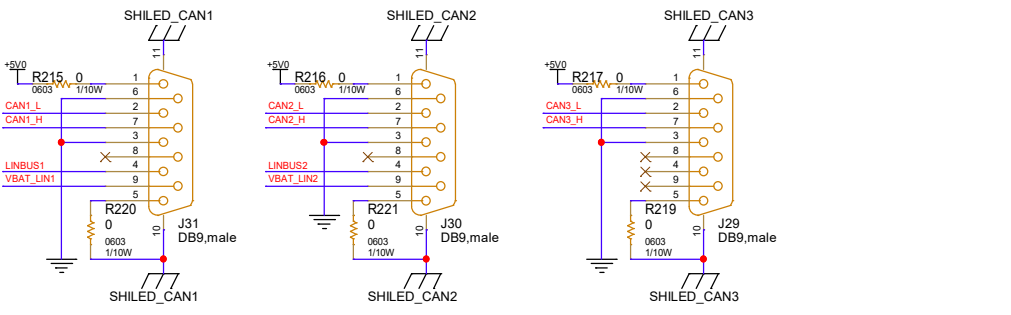
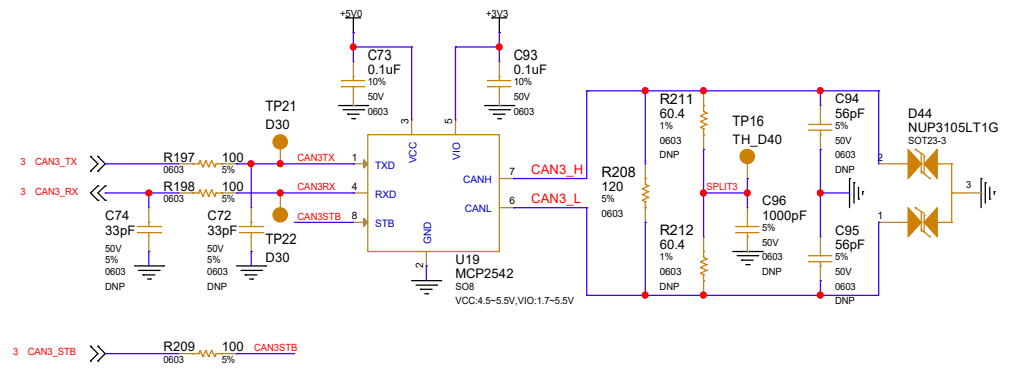
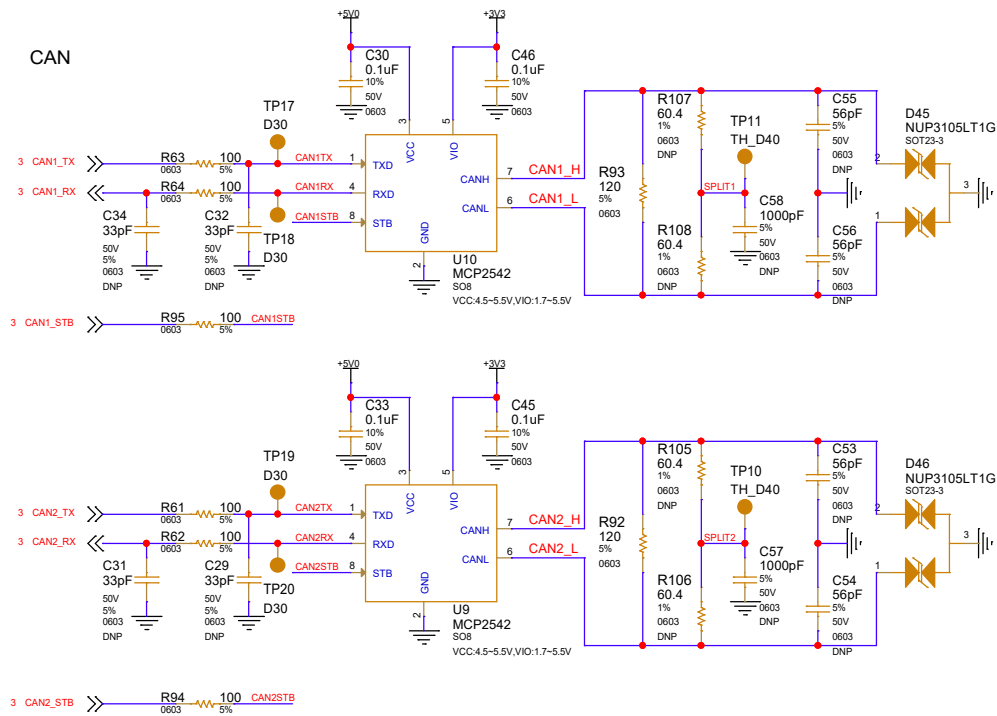
I2C
EEPROM

QSPI FLASH

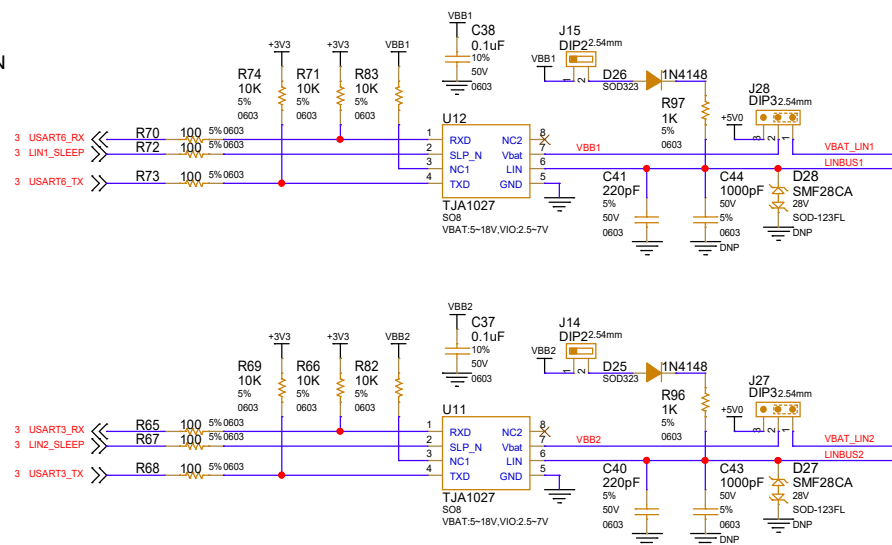




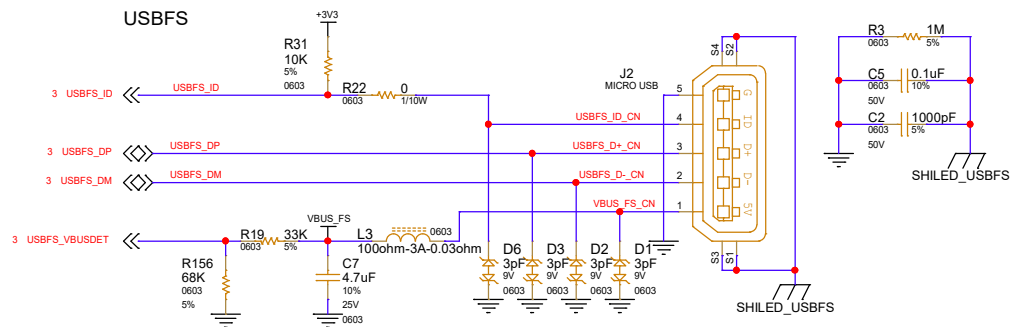
CAN



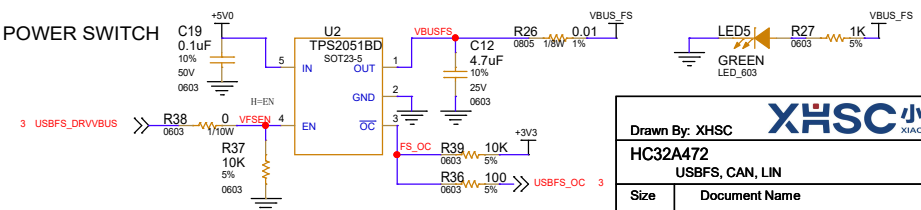
LIN

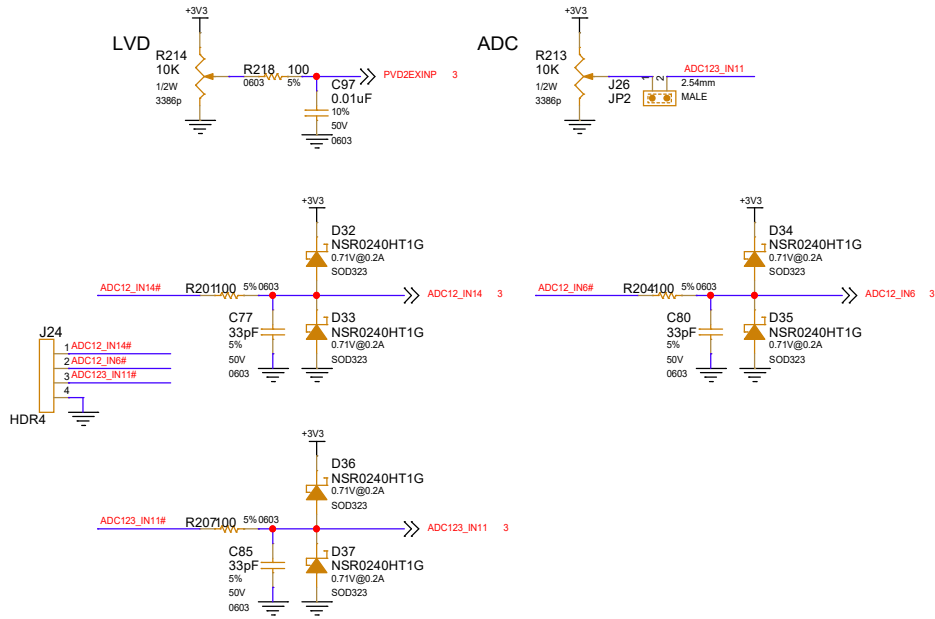


USBFS

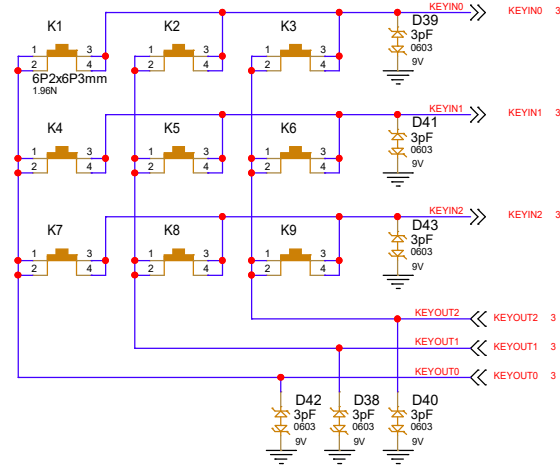


POWER SWITCH

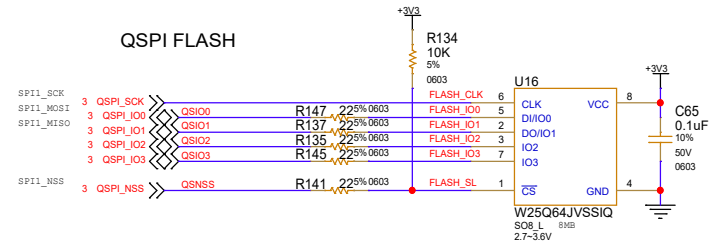




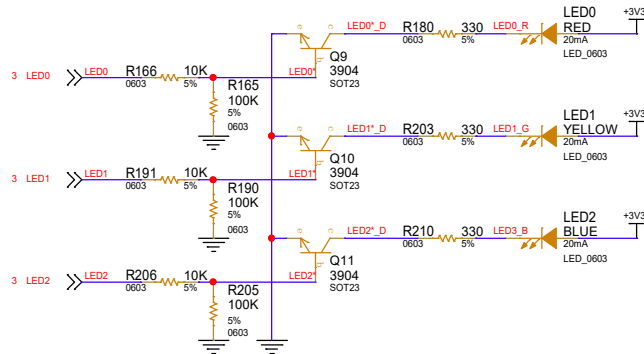
MATRIX KEYBOARD



QSPI FLASH



LEDS



Drawn By: XHSC		XHSC 小华半导体 XIAOHUA SEMICONDUCTOR	
HC32A472			
ADC,KEYS,LEDS,QSPI FLASH			
Size	Document Name		Rev
B	EV_A472_LQ100		1.0
Date:	Monday, October 14, 2024	Sheet:	6 of 7

