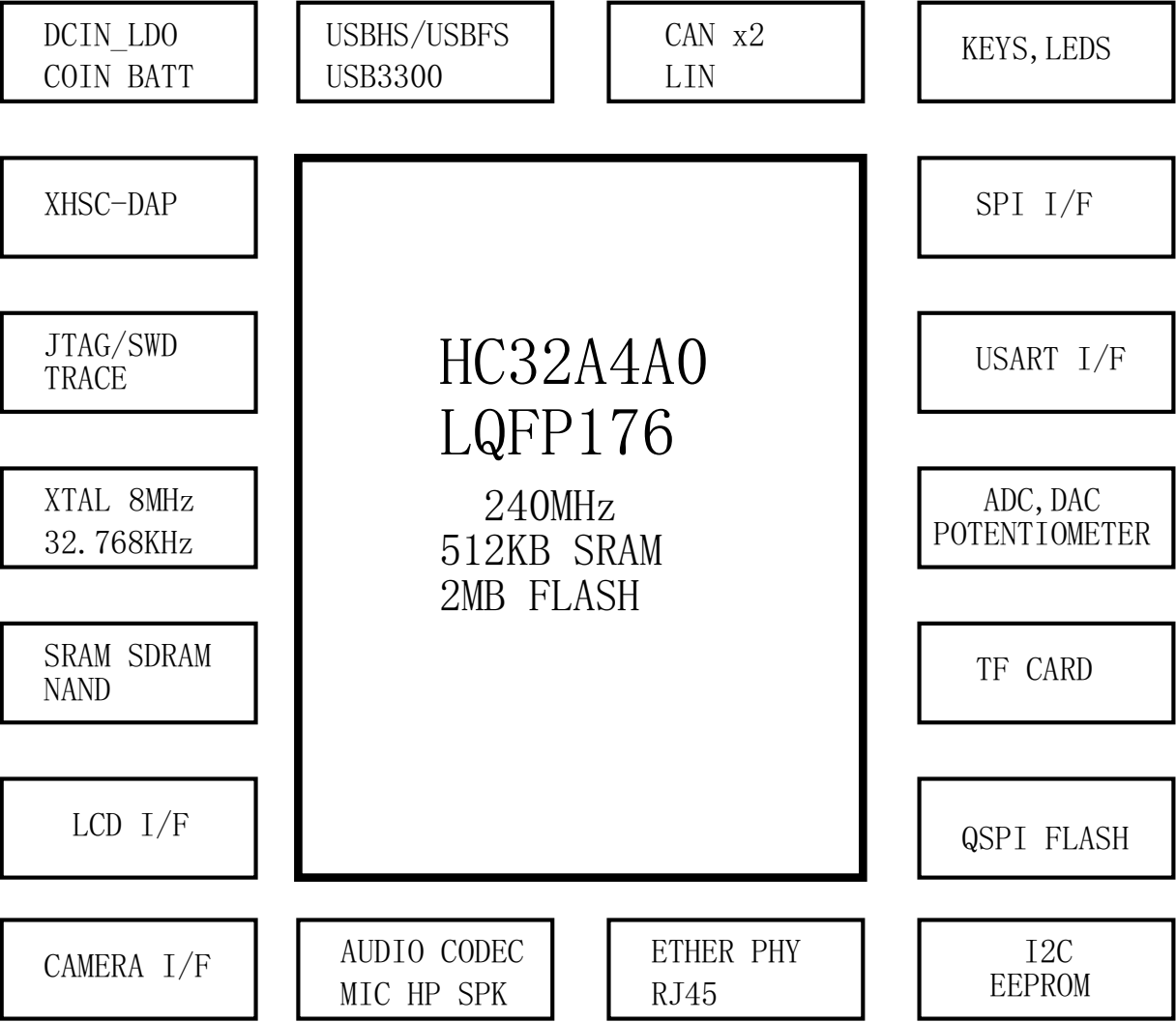


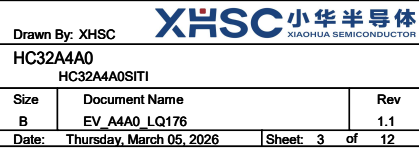
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Revision History

Rev	Date	Description
1.0	02/23/2022	- Initial release.
1.1	11/19/2024	- Update HC32A4A0 pin definition.

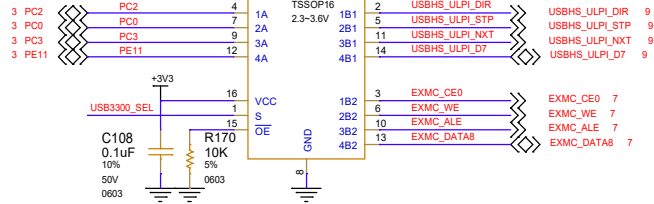




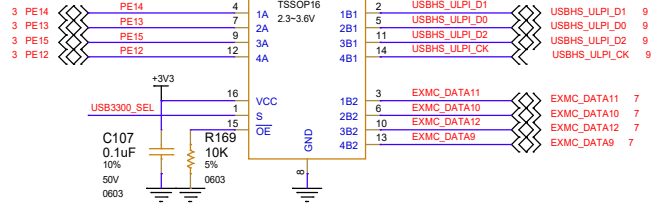
PIN MUX

SPDT
V_{IH} > 2V, V_{IL} < 0.8V
L → A=B1
H → A=B2 (default)

U12
SN74CB3Q3257
TSSOP16
2.3~3.6V

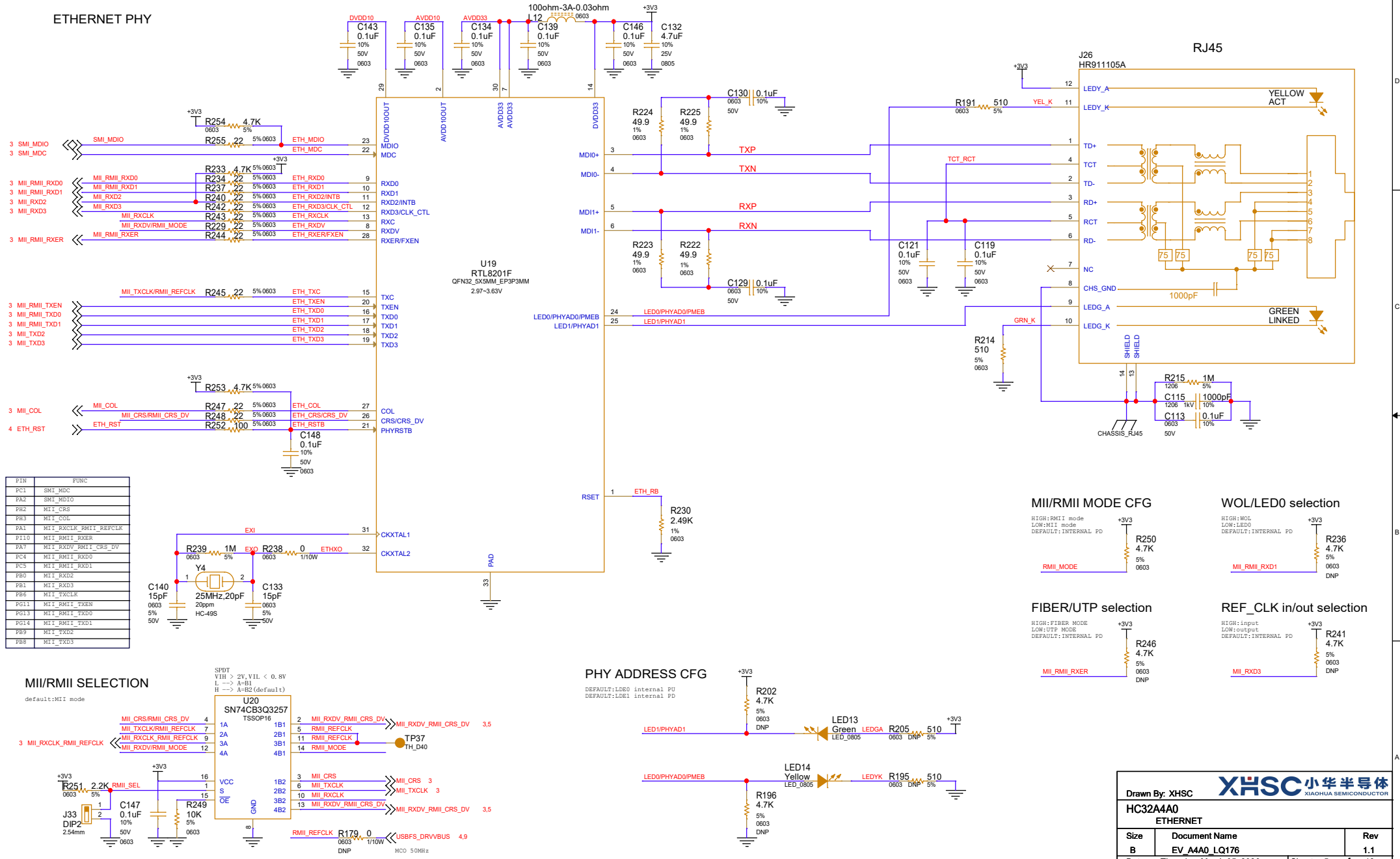


U11
SN74CB3Q3257
TSSOP16
2.3~3.6V

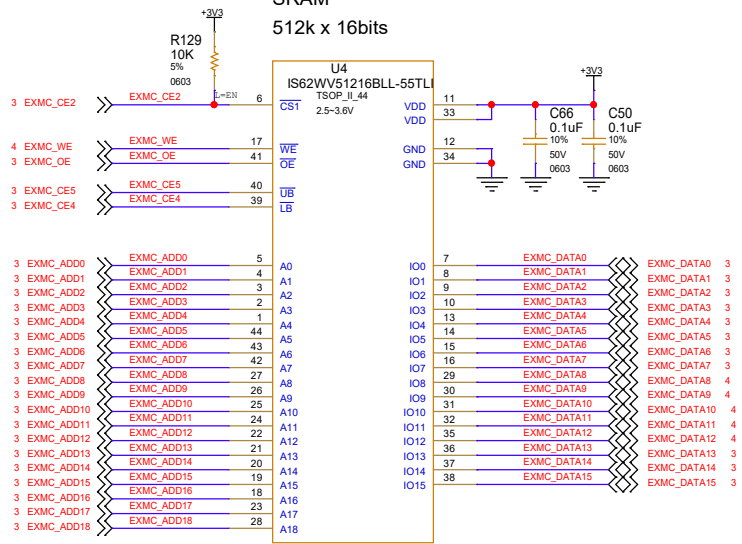


PIN	DEFAULT	USB3300 DETECTED	USBHS DETECTED
PC0	EXMC_WE	USBHS_ULPI_STP	
PC2	EXMC_CE0	USBHS_ULPI_DIR	
PC3	EXMC_ALE	USBHS_ULPI_NXT	
PE11	EXMC_DATA8	USBHS_ULPI_D7	
PE12	EXMC_DATA9	USBHS_ULPI_D6	
PE13	EXMC_DATA10	USBHS_ULPI_D5	
PE14	EXMC_DATA11	USBHS_ULPI_D4	
PE15	EXMC_DATA12	USBHS_ULPI_D3	
PE16	EXMC_DATA13	USBHS_ULPI_D2	
PE17	EXMC_DATA14	USBHS_ULPI_D1	
PE18	EXMC_DATA15	USBHS_ULPI_D0	
PE19	EXMC_DATA16	USBHS_ULPI_D7	
PE20	EXMC_DATA17	USBHS_ULPI_D6	
PE21	EXMC_DATA18	USBHS_ULPI_D5	
PE22	EXMC_DATA19	USBHS_ULPI_D4	
PE23	EXMC_DATA20	USBHS_ULPI_D3	
PE24	EXMC_DATA21	USBHS_ULPI_D2	
PE25	EXMC_DATA22	USBHS_ULPI_D1	
PE26	EXMC_DATA23	USBHS_ULPI_D0	
PE27	EXMC_DATA24	USBHS_ULPI_D7	
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PE30	EXMC_DATA27	USBHS_ULPI_D4	
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PE37	EXMC_DATA34	USBHS_ULPI_D5	
PE38	EXMC_DATA35	USBHS_ULPI_D4	
PE39	EXMC_DATA36	USBHS_ULPI_D3	
PE40	EXMC_DATA37	USBHS_ULPI_D2	
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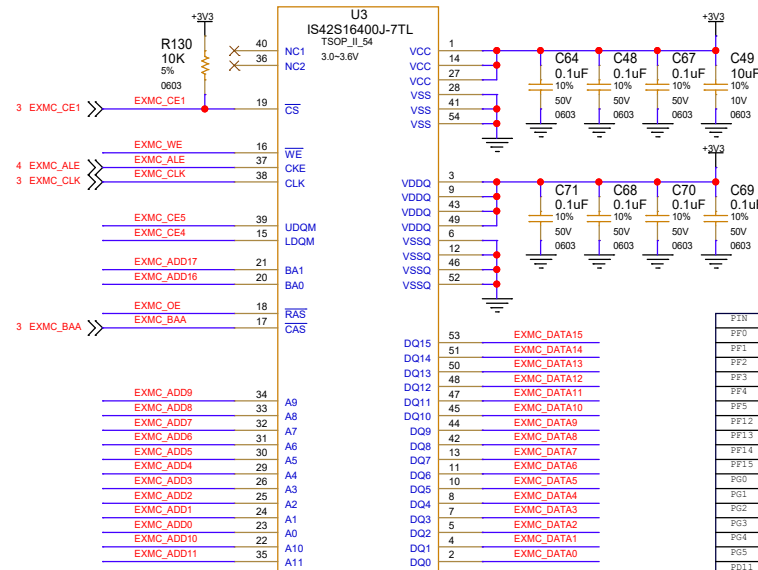
ETHERNET PHY



SRAM 512k x 16bits

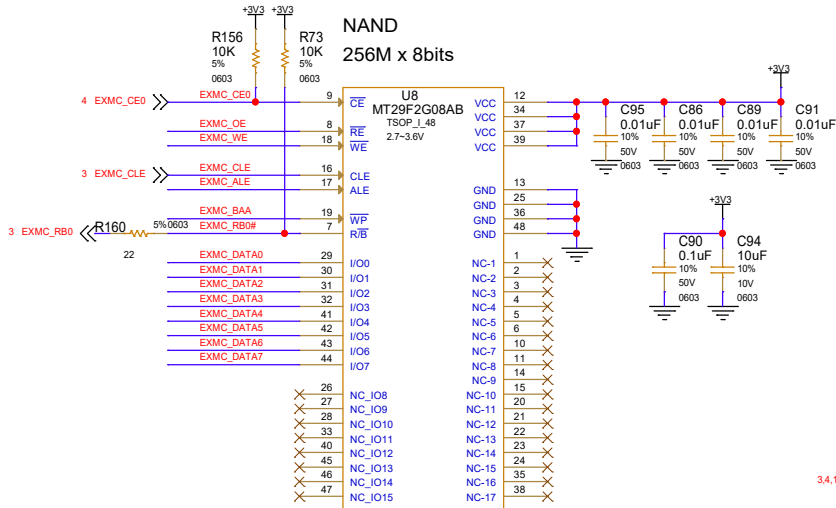


SDRAM 1M x 16bits x 4Banks

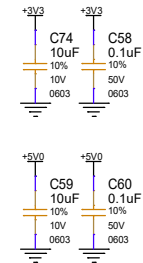
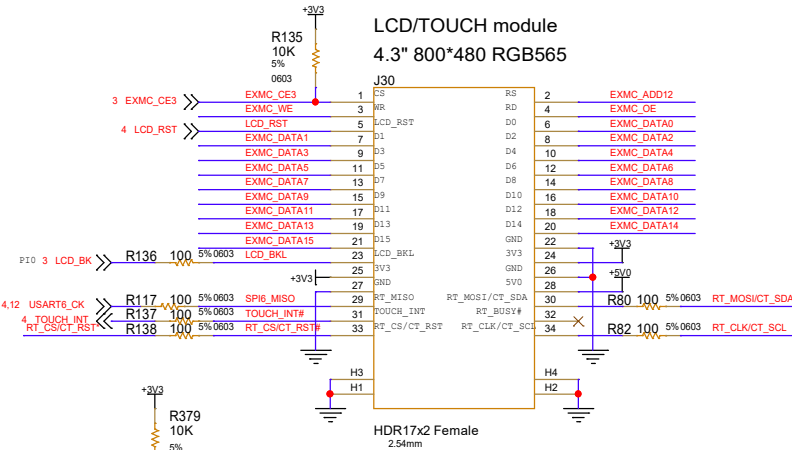


PIN	FUNC	PIN	FUNC	PIN	FUNC
PF0	EXMC_ADD0	PD14	EXMC_DATA0	PC2	EXMC_CE0
PF1	EXMC_ADD1	PD15	EXMC_DATA1	PG9	EXMC_CE1
PF2	EXMC_ADD2	PD0	EXMC_DATA2	PG10	EXMC_CE2
PF3	EXMC_ADD3	PD1	EXMC_DATA3	PG12	EXMC_CE3
PF4	EXMC_ADD4	PD7	EXMC_DATA4	PE0	EXMC_CE4
PF5	EXMC_ADD5	PD8	EXMC_DATA5	PE1	EXMC_CE5
PF12	EXMC_ADD6	PD9	EXMC_DATA6	PC0	EXMC_WE
PF13	EXMC_ADD7	PD10	EXMC_DATA7	PF11	EXMC_OE
PF14	EXMC_ADD8	PD11	EXMC_DATA8	PC3	EXMC_ALE
PF15	EXMC_ADD9	PD12	EXMC_DATA9	PD12	EXMC_CLE
PG0	EXMC_ADD10	PD13	EXMC_DATA10	PG15	EXMC_BAA
PG1	EXMC_ADD11	PD14	EXMC_DATA11	PG6	EXMC_RB0
PG2	EXMC_ADD12	PD15	EXMC_DATA12	PG8	EXMC_CLK
PG3	EXMC_ADD13	PD0	EXMC_DATA13		
PG4	EXMC_ADD14	PD1	EXMC_DATA14		
PG5	EXMC_ADD15	PD2	EXMC_DATA15		
PD11	EXMC_ADD16				
PD12	EXMC_ADD17				
PD13	EXMC_ADD18				

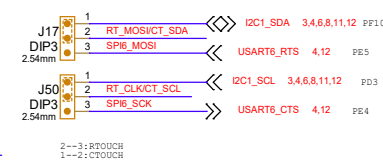
NAND 256M x 8bits



LCD/TOUCH module 4.3" 800*480 RGB565

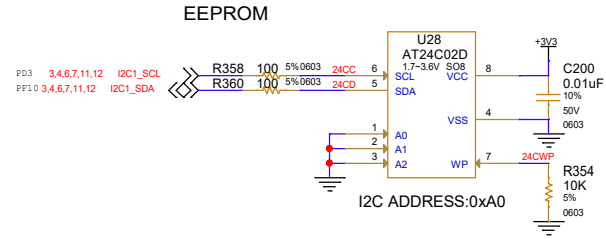
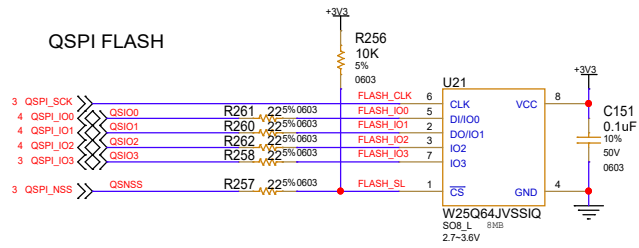


TOUCH selection

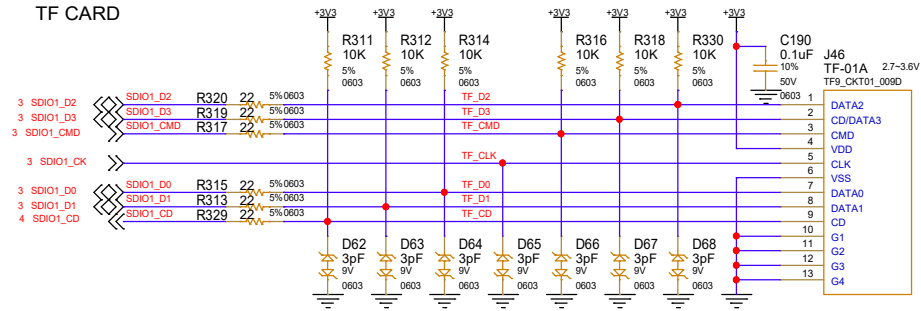


Drawn By: XHSC			XHSC 小华半导体	
HC32A4A0			XIAOHUA SEMICONDUCTOR	
SRAM, SDRAM, NAND, LCD				
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PIN	FUNC
PB13	QSPI_FIO0
PB12	QSPI_FIO1
PB10	QSPI_FIO2
PB2	QSPI_FIO3
PC6	QSPI_SCK
PC7	QSPI_NSS

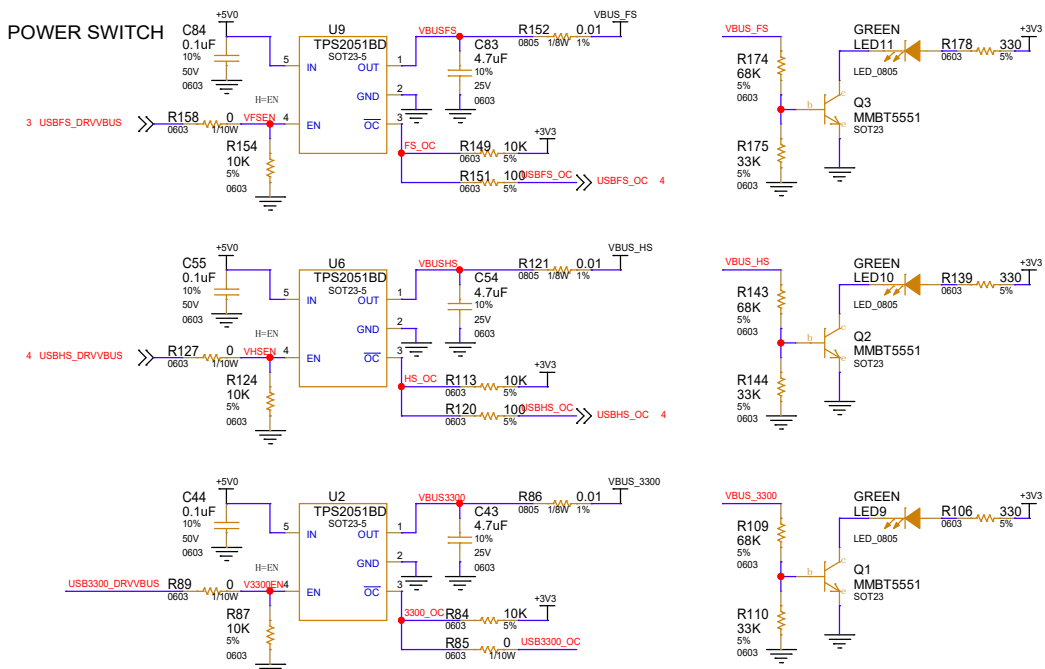


PIN	FUNC
PB7	SDIO1_D0
PA8	SDIO1_D1
PC10	SDIO1_D2
PB5	SDIO1_D3
PD2	SDIO1_CMD
PC12	SDIO1_CK

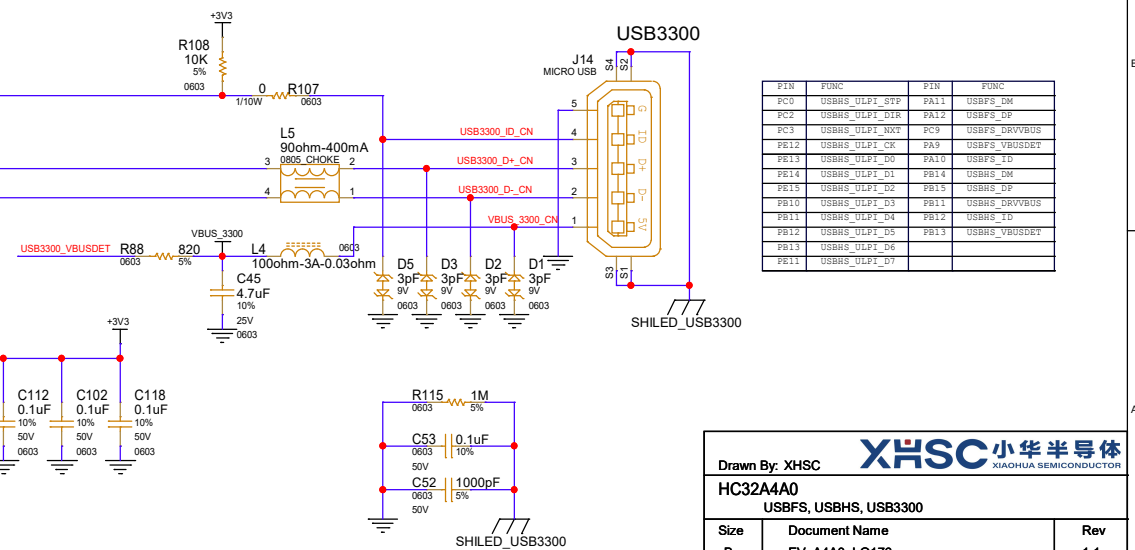
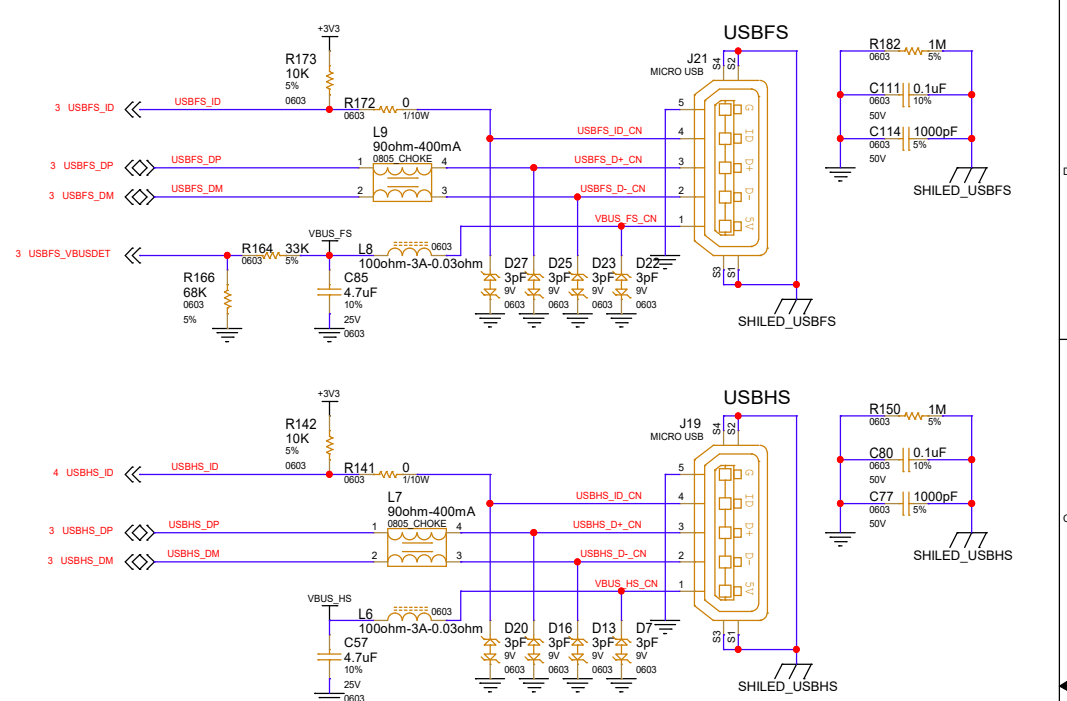
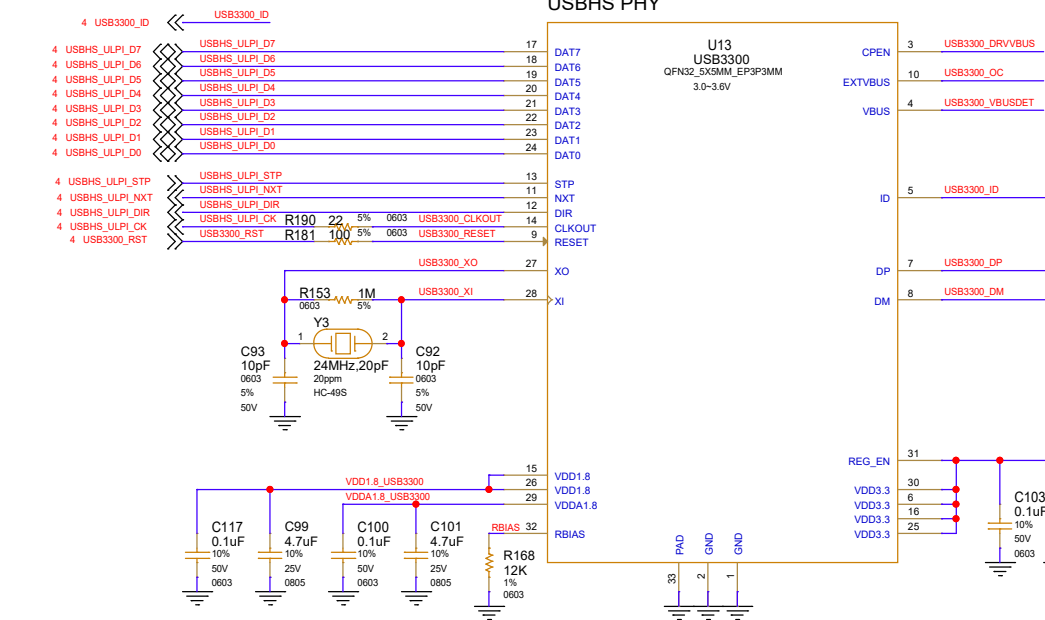


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HC32A4A0		
QSPI FLASH, EEPROM, TF CARD		
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POWER SWITCH

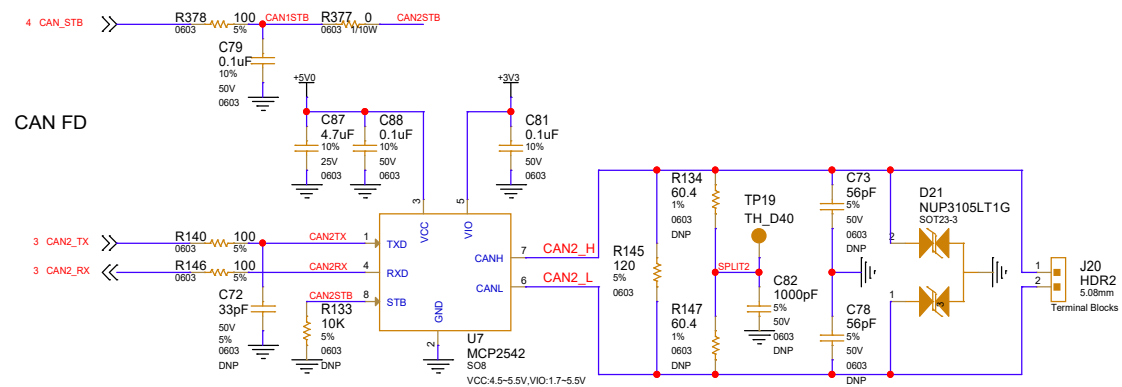
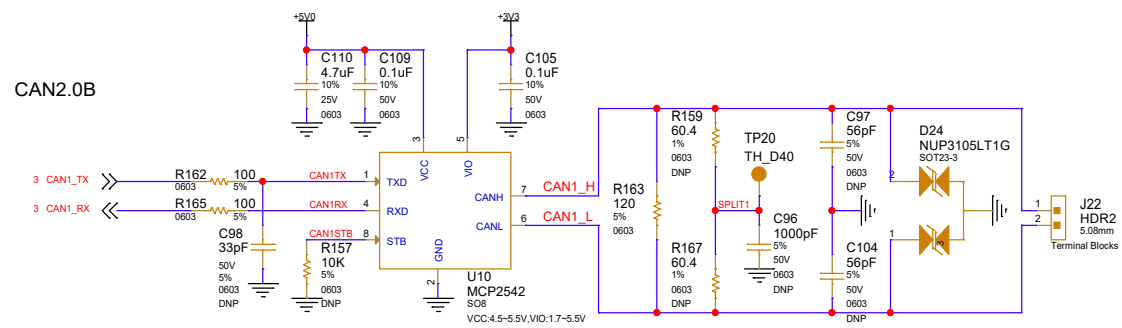


USBHS PHY

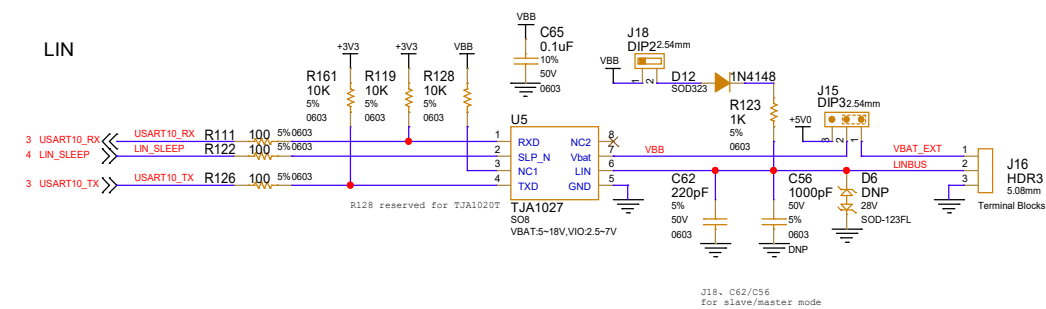


PIN	FUNC	PIN	FUNC
PC0	USBS_ULPI_STP	PA11	USBS_DM
PC2	USBS_ULPI_DIR	PA12	USBS_DP
PC3	USBS_ULPI_NXT	PC9	USBS_DPVBUS
PE12	USBS_ULPI_CR	PA9	USBS_VBUSDET
PE13	USBS_ULPI_D0	PA10	USBS_ID
PE14	USBS_ULPI_D1	PA14	USBS_DM
PE15	USBS_ULPI_D2	PE15	USBS_DP
PE10	USBS_ULPI_D3	PE11	USBS_DPVBUS
PE11	USBS_ULPI_D4	PE12	USBS_ID
PE12	USBS_ULPI_D5	PA13	USBS_VBUSDET
PE13	USBS_ULPI_D6		
PE11	USBS_ULPI_D7		

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HC32A4A0			
USBFS, USBHS, USB3300			
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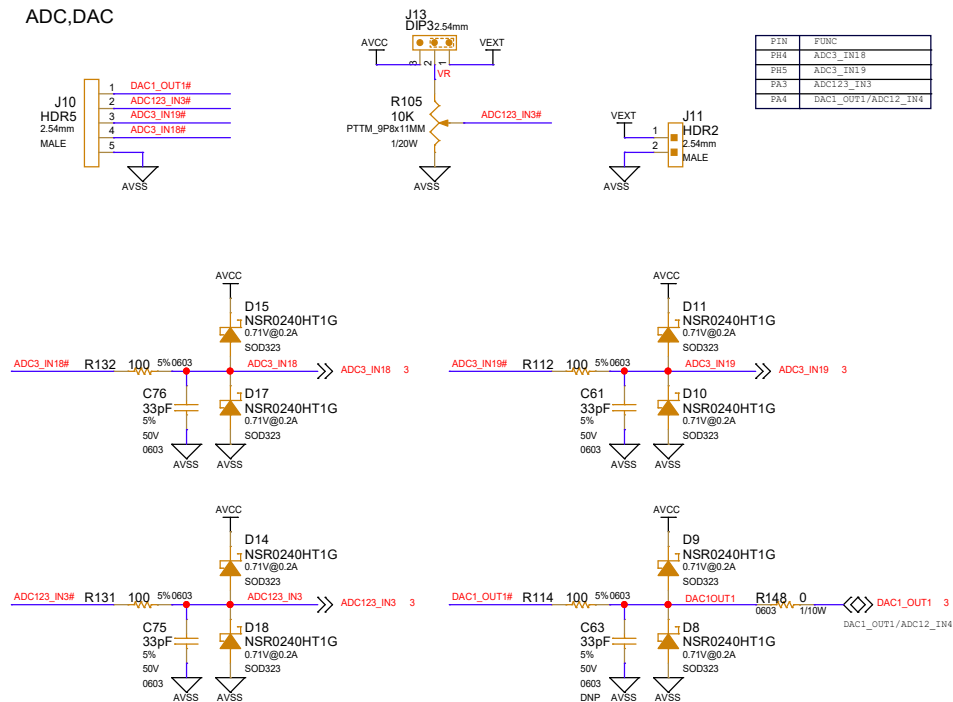


PIN	FUNC
PD4	CAN1_RX
PD5	CAN1_TX
PD6	CAN2_RX
PD7	CAN2_TX
PI9	USART10_RX
PI11	USART10_TX



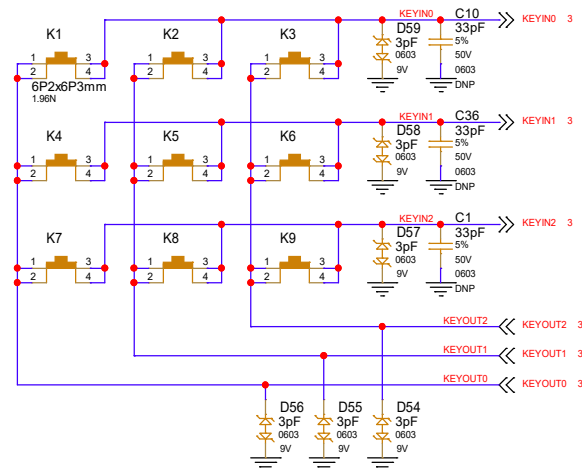
J18、C62/C56
for slave/master mode

ADC,DAC



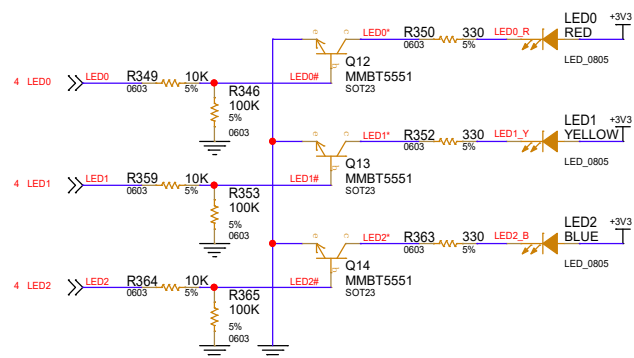
PIN	FUNC
PA4	ADC3_IN18
PA5	ADC3_IN19
PA3	ADC12_IN3
PA4	DAC1_OUT1/DAC12_IN4

MATRIX KEYBOARD

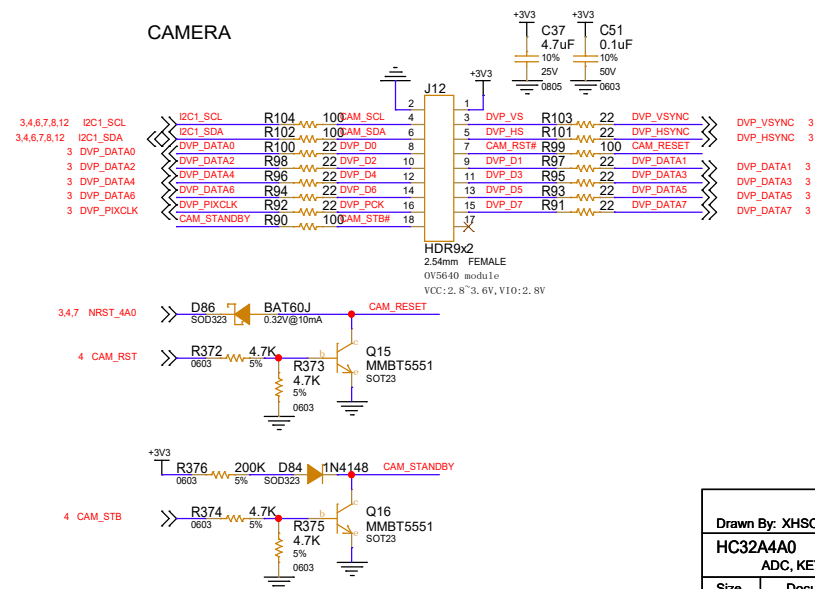


PIN	FUNC
PI8	KEYIN0
PI3	KEYIN1
PI7	KEYIN2
PC11	KEYOUT0
PC8	KEYOUT1
PA6	KEYOUT2

LEDS



CAMERA



PIN	FUNC
PF9	DVP_PIXCLK
PI5	DVP_VSYNC
PI8	DVP_HSYNC
PI9	DVP_DATA0
PI10	DVP_DATA1
PI11	DVP_DATA2
PI12	DVP_DATA3
PI14	DVP_DATA4
PI4	DVP_DATA5
PI6	DVP_DATA6
PI7	DVP_DATA7
PD3	I2C1_SCL
PF10	I2C1_SDA

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XHSC 小华半导体
XIAOHUA SEMICONDUCTOR

HC32A4A0

ADC, KEYS, LEDS, CAMERA

Size

Document Name

Rev

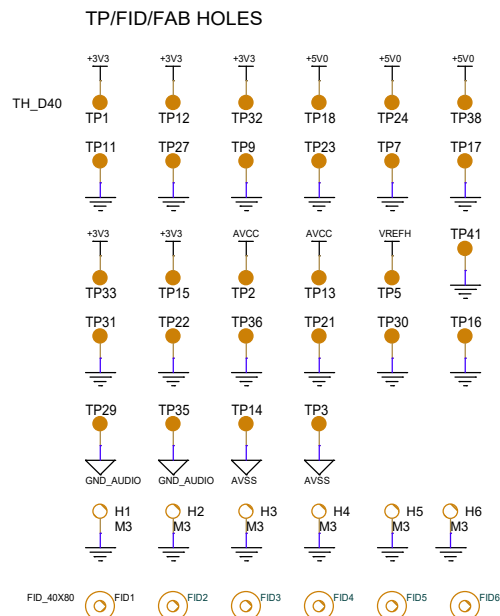
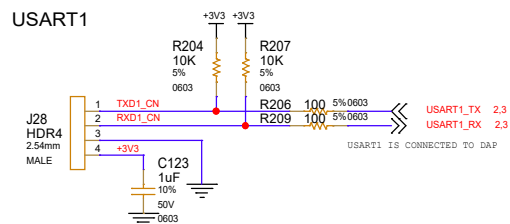
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